

EAST

	L #	Hits	Search Text	DBs	Time Stamp	Type
1	L1	89125	(IC OR integrated ADJ circuit) WITH (design\$3 OR order\$3 OR custom\$)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/16 19:00	BRS
2	L2	25558	L1 SAME (factory OR production OR assembly OR manufactur\$3 OR fabricat\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/16 19:01	BRS
3	L4	743	L2 SAME (www OR "world wide web" OR internet OR intranet OR network)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/16 19:02	BRS
4	L5	257	L4 AND (db OR database OR "data base")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/16 19:37	BRS
5	L6	56	L4 SAME (db OR database OR "data base")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/16 19:03	BRS
6	L7	76	L5 AND (CAD OR CAM OR CAE OR computer ADJ aided ADJ (design\$3 OR draft\$3 OR draw\$3 OR manufactur\$3 OR machin\$3 OR engineer\$))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/16 19:38	BRS
7	L8	64	L7 NOT L6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/16 19:39	BRS
8	L9	12	L7 AND L6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/16 19:39	BRS

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	Document ID	Source	Issue Date	Title	Current	Inventor	Image	Doc.	D	P
1	US 6725237 B2	USPAT	20040420	System and method of preparing data for a semiconductor mask manufacturer	707/104.1	Clairmont, James B. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	US 6668360 B1	USPAT	20031223	Automatic integrated circuit design kit qualification service provided through the internet	716/4	Liu, Louis	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	US 6622295 B1	USPAT	20030916	Network-based photomask data entry interface and instruction generator for manufacturing photomasks	716/19	Schepp, Jeffry S. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	US 6594799 B1	USPAT	20030715	Method and system for facilitating electronic circuit and chip design using remotely located resources	716/1	Robertson, William H. et al.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5	US 20040117374 A1	US-PG PUB	20040617	Customized design portfolio integrating IP libraries and technology documents	707/10	Hung, Lup Cheong Patrick et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
6	US 20040107214 A1	US-PG PUB	20040603	Customized document portfolio system integrating IP libraries and technology documents	707/104.1	Hung, Lup Cheong Patrick et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
7	US 20040098391 A1	US-PG PUB	20040520	Method and system for facilitating electronic circuit and chip design using remotely located resources	707/10	Robertson, William H. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
8	US 20040054982 A1	US-PG PUB	20040318	Network-based photomask data entry interface and instruction generator for manufacturing photomasks	716/21	Schepp, Jeffry S. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
9	US 20030126153 A1	US-PG PUB	20030703	System and method of preparing data for a semiconductor mask manufacturer	707/104.1	Clairmont, James B. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
10	US 20020188910 A1	US-PG PUB	20021212	Method and system for chip design using remotely located resources	716/1	Zizzo, Claudio	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
11	US 20020081501 A1	US-PG PUB	20020627	Device manufacturing method, photomask used for the method, and photomask manufacturing method	430/5	Hasegawa, Norio et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
12	US 20020016948 A1	US-PG PUB	20020207	Method and configuration for verifying a layout of an integrated circuit and application thereof for fabricating the integrated circuit	716/1	Rein, Achim et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
13	US 20010044667 A1	US-PG PUB	20011122	System of manufacturing semiconductor integrated circuit	700/100	Nakano, Hirotaka et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

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	L #	Hits	Search Text	DBs	Time Stamp	Type
1	L10	50	("5201071" "5728598" "4872011" "4905006" "5847609" "4323731" "4472831" "4479233" "4494137" "4535470" "4536885" "4541109" "4589127" "4757539" "5027186" "5253298" "5289502" "5629570" "5731740" "6107700" "6256482" "6362668" "6421278" "6476632" "4554873" "4785453" "5453978" "5466619" "5583764" "5606703" "5689414" "5721957" "5838589" "5926583" "6038350" "6249913" "6314442" "6343323" "6532163" "6574652" "4426728" RE33381 "3676791" "4079204" "4105868" "4248986" "4259446" "4373115" "4769842" "4850020").pn.	USPAT	2005/03/16 19:42	BRS

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Most Frequently Occurring Classifications of Patents Returned
From A Search of 10675690 on March 15, 2005

Original Classifications

8 381/15
7 381/16
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2 382/304

Cross-Reference Classifications

2 327/46
2 332/145
2 332/159
2 348/721
2 358/443
2 358/447
2 381/15
2 455/102
2 455/205
2 700/4

Combined Classifications

10 381/15
8 381/16
2 327/46
2 332/145
2 332/159
2 341/150
2 348/721
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2 358/447
2 382/304
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2 455/205
2 700/286
2 700/4
2 702/2